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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number		10/723,948	
				Filing Date		11/26/2003	
				First Named Inventor:		Cornel D. Stanescu	
				Examiner: Y. J. Han		GROUP: 2838	
Sheet	1	of	1	Attorney Docket Number		CAT-12504-1C	
U.S. PATENT DOCUMENTS							
Examiner Initials*	Cited No. ¹	Document Number Number - Kind Code ^{2(f)(known)}		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	
JP	C01	US-2004/0108844	A1	06/2004	Stanescu et al.		
	C02	US-5,717,323		02/1998	Tailliet		
	C03	US-6,201,491	B1	03/2001	Brunolli et al.		
	C04	US-6,320,451	B1	11/2001	Harvey et al.		
	C05	US-6,555,996	B2	04/2003	Drori et al.		
		US-					
		US-					
		US-					
FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document Cntry Code ³ Number ⁴ Kind Code ^{5(f)(known)}		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T⁶
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER SIGNATURE: J. Han				DATE CONSIDERED: 3/4/05			

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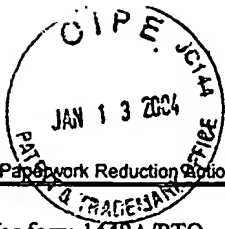
Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	Filed Herewith 10723948		
		Filing Date	November 26, 2003		
		First Named Inventor:	STANESCU, Cornel D.		
		Examiner name: Unknown	GROUP: Unknown 2838		
Sheet	1	of	1	Attorney Docket Number	CAT-12504-1C

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JS	A01	4,271,486		Dagostino et al.	6/2/1981	
	A02	4,468,607		Tanaka et al.	8/28/1984	
	A03	4,668,932		Drori et al.	5/26/1987	
	A04	5,084,667		Drori et al.	1/28/1992	
	A05	5,297,056		Lee et al.	3/22/1994	
	A06	5,495,245		Ashe	2/27/1996	
	A07	6,201,491	B1	Brunolli et al.	3/13/2001	
↓	A08	6,331,768	B1	Drori et al.	12/18/2001	

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JS	Catalyst Semiconductor, Inc., "CAT515: 8-Bit Quad DACpot with RDY/BUSY and IND. Reference Inputs," 1997, pp. 1-11. (No month)
	Catalyst Semiconductor, Inc., "CAT524: 8-Bit Quad Digital POT," 1998, pp. 25-36. (No month)
	Catalyst Semiconductor, Inc., "CAT514: 8-Bit Quad DACpot," 1997, pp. 1-13. (No month)
	Catalyst Semiconductor, Inc., "CAT514: 8-Bit Quad Digital POT," 1999, pp. 1-12. (No month)
	Catalyst Semiconductor, Inc., "CAT525: 8-Bit Quad Digital POT With Independent Reference Inputs," 1999, pp. 1-11. (No month)
	Catalyst Semiconductor, Inc., "CAT5111, CAT5112, CAT5113 and CAT5114: 100/32-Tap Digital POT with Independent Reference Inputs," 1999, pp. 1-6. (No month)
↓	Stanescu et al., "32 Taps Digitally Controlled Buffered Potentiometer," Proceedings of CAS 2001 International Semiconductor Conference, IEEE Catalog Number 01TH8547, pp. 535-538, October 2001.
EXAMINER	J. Han
DATE CONSIDERED	3/4/05

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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.



PTO/SB/08A (08-03)

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GH	A01	US-6,320,451	11/2001	Harvey et al.	
AI	A02	US-5,717,323	02/1998	Tailiet	

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